

## DVAD41 - Introduction to Data Plane Programming

# Webinar 4 - ECN and Advanced P4 concepts



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#### Announcement

- There will be an assignment in the week after the module 1 in DVAD40 course.
  - Unlock: Mar 15, 12am
  - Handin: Mar 22, 11:55pm
- If you need separate assignment for DVAD41 course module, please let me know.



## **Exercise 2: Recap**

basic\_tunnel



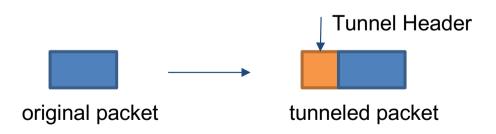
## **Basic Tunneling**

#### Tunneling main feature for

- Data Center networks
- Mobile Core Networks (e.g. Evolved Packet Core EPC)
- Network Virtualization (e.g. VXLAN, GRE, ...)
- Mobility Management (e.g. Mobile IP)
- Overlay Routing
- o ...

#### •How can we implement tunneling?

o encapsulate a packet into another one by prepending a new header



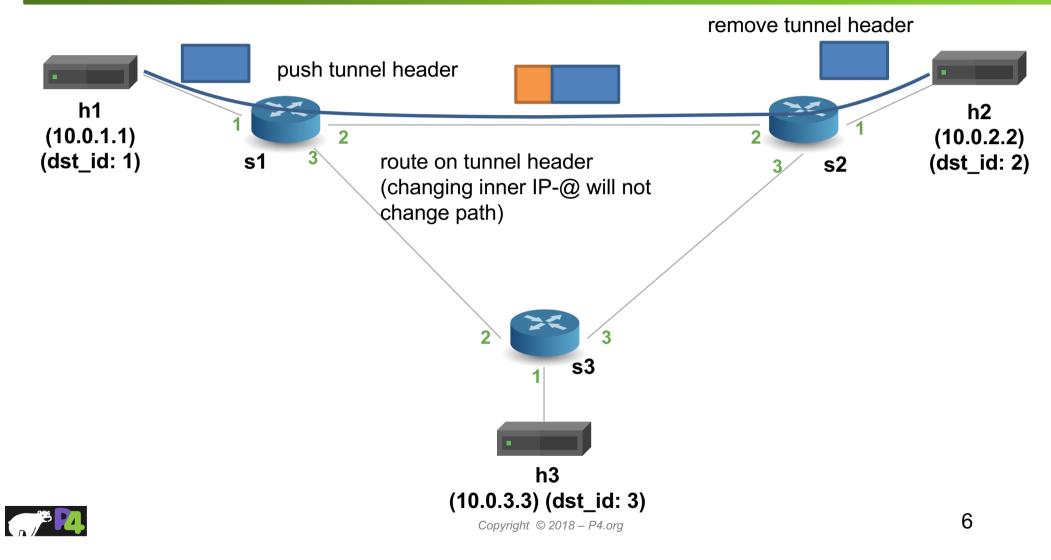


## **Basic Tunneling**

- ToDo: Add support for basic tunneling to the basic IP router in P4
- Define a new header type (myTunnel) to encapsulate the IP packet
  myTunnel header includes:
  - o proto\_id : type of packet being encapsulated
  - o dst\_id : ID of destination host
- Modify the switch to do routing using the myTunnel header



## **Basic Forwarding: Topology**



## **Basic Tunneling TODO List**

- **Define** myTunnel\_t header type and add to headers struct
- Update parser based on ethertype (0x1212: tunnel)
- **Define** myTunnel\_forward action
- **Define** myTunnel\_exact table
- Update table application logic in MyIngress apply statement
- Update deparser
- Adding forwarding rules
  - myTunnel\_forward rule to forward packets on the tunnel header



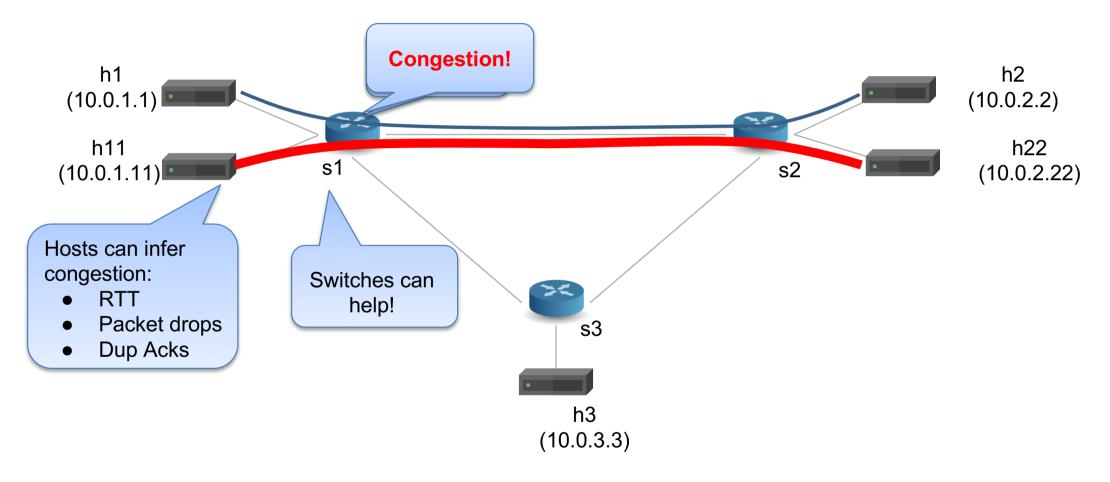
## **Exercise 3: Monitoring & Debugging**

#### **Explicit Congestion Notification - ECN**



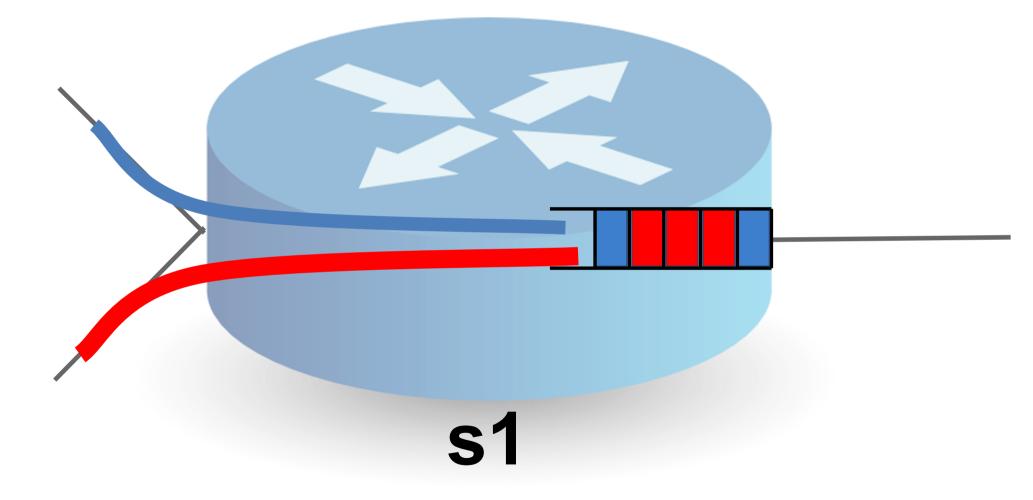
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## **Monitoring & Debugging**



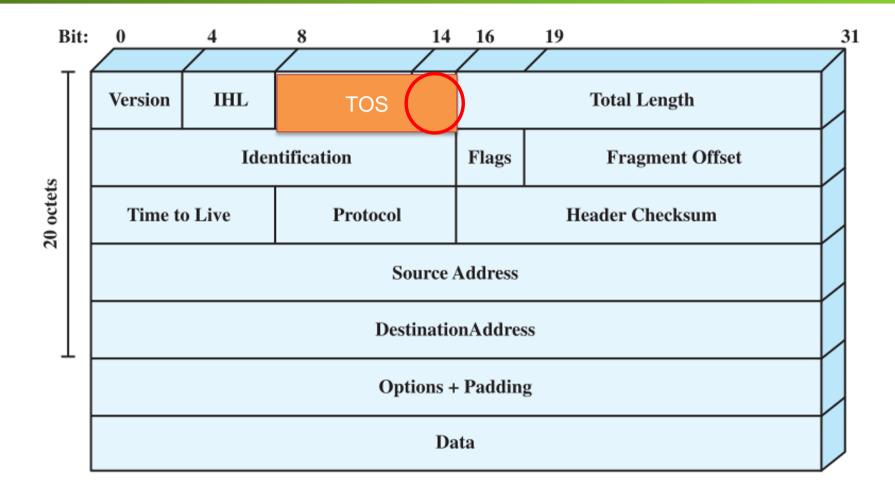


## **Monitoring & Debugging**





## **Explicit Congestion Notification**





## **Explicit Congestion Notification**

#### Explicit Congestion Notification

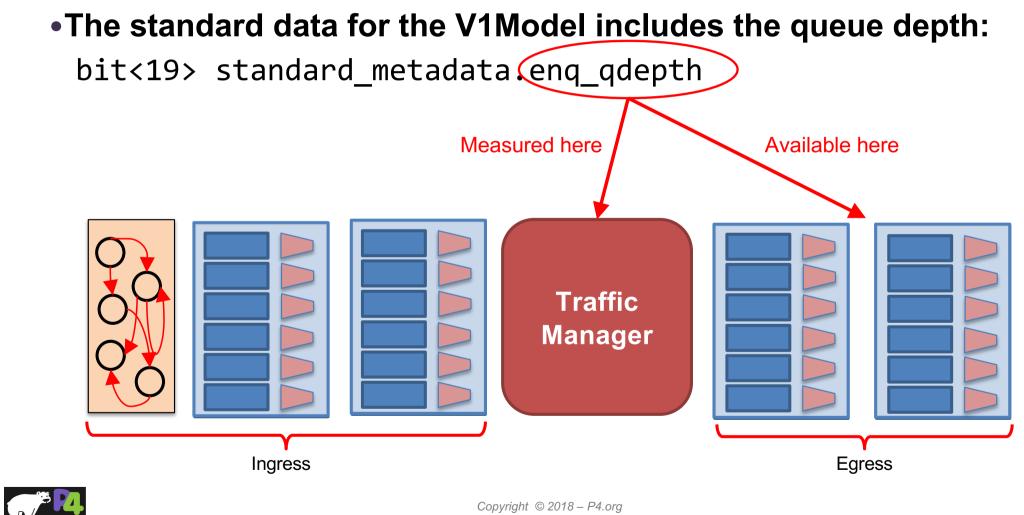
- ∘00: Non ECN-Capable Transport, Non-ECT
- 10: ECN Capable Transport, ECT(0)
- ○01: ECN Capable Transport, ECT(1)
- °11: Congestion Encountered, CE

## •For packets originating from ECT, ECN-capable switches set the CE bit upon congestion

E.g., observed queue depth > threshold
more details: IETF RFC 3168
https://tools.ietf.org/html/rfc3168



## **Explicit Congestion Notification in P4**





## **ECN marking**

•ToDo: Add support for ECN marking to the basic IP router

#### •Desired behavior:

- If an end-host supports ECN, it puts the value of 1 or 2 in the ipv4.ecn field.
- For such packets, each switch may change the value to 3 if the queue size is larger than a threshold.
- The receiver copies the value to sender, and the sender can lower the rate.



## **ECN marking in P4**

## modify ipv4\_t to split TOS into DiffServ and ECN update checksum accordingly

- In egress, compare queue length with ECN\_THRESHOLD
  - o if queue is larger, set ECN bits to 3 (bin 11) (congestion encountered)
  - o do this only if end-host supports by having set original ECN to 1 or 2
- Define an action to drop a packet which calls mark\_to\_drop();
- Define an egress control block that checks the ECN and standard\_metadata.enq\_qdepth and sets the ipv4.ecn accordingly
- test your solution by redirecting the receive.py to a log file to check the TOS



#### **Advanced P4 Constructs**

Data Types, Externs, Registers, Meters, etc.



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## **Different Data Types in P4**

bool	Boolean values
bit <w></w>	Bit-strings of width W
int <w></w>	Signed integer of width W
varbit <w></w>	Bit-string with dynamic length (max W)
<del>float</del>	no support
string	no support

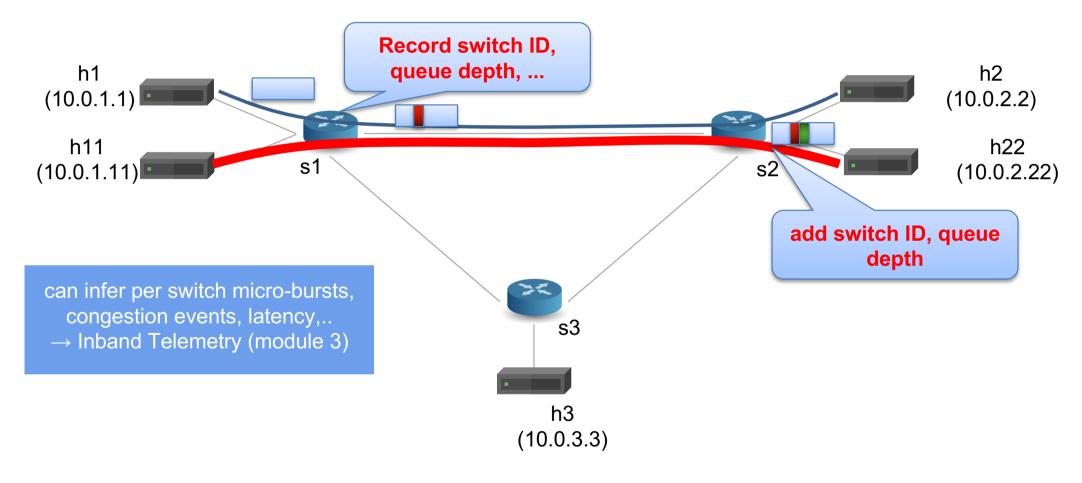


## **Operators to define composed types in P4**

Header	Header Union	Header Stack
<pre>header Ethernet_h {     bit&lt;48&gt; dstAddr;     bit&lt;48&gt; srcAddr;     bit&lt;16&gt; etherType; }</pre>	header_union ip_h { IPv4_h v4; IPv6_h v6; }	<pre>header mpls_h {     bit&lt;20&gt;    label;     bit&lt;3&gt;    tc;     bit    BoS;     bit&lt;8&gt;    ttl; }</pre>
We have shown already struct	either IPv4 or IPv6 header	mpls_h[8] mpls; Array of up to 8 MPLS headers



#### **Example Use of Header Stacks - MultiRoute Inspect**





## **Multi-Route Inspect: Packet Format**

header	mri_t { bit<16>   cc	ount;
}		
header	switch_t {	
swi	tchID_t swi	d;
qde	pth_t qde	pth;
}		
struct	headers {	
	ethernet_t	ethernet;
	ipv4_t	ipv4;
	<pre>ipv4_option_t</pre>	<pre>ipv4_option;</pre>
	mri_t	mri;
	switch_t[MAX_H	HOPS] swtraces;
}		

•	Hea	der validity operations:
	0	hdr.setValid():add_header
	0	hdr.setInvalid():remove_heade
	0	hdr.isValid():test validity
•	Hea	ider Stacks
	0	hdr[CNT] stk;
•	Hea	der Stacks in Parsers
	0	stk.next
	0	stk.last
	0	<pre>stk.lastIndex</pre>
•	Hea	der Stacks in Controls
	0	<pre>stk[i]</pre>
	0	stk.size
	0	<pre>stk.push_front(int count)</pre>
	0	<pre>stk.pop_front(int count)</pre>
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## **State Management in P4**

#### Stateless Objects

Variables (metadata), headers,..do not maintain state across packets

#### Stateful Objects

Tables

• Externs in P4-14: Counters, Meters, ...keep state across different packets

Object	Data Plane Interface		Control Plane Can	
	Read State	Modify/Write State	Read	Modify/Write
Table	apply()		Yes	Yes
Parser Value Set	get()		Yes	Yes
Counter		count()	Yes	Yes* WriteRequest with the MODIFY
Meter	execute ()		Configuration Only	Configuration Only
Register	read()	write()	Yes	Yes
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## **P4 Registers**

Store arbitrary data (single values or arrays of N entries)
Definition:

```
register<Type>(N) reg;
```

•writing:

```
reg.write(n, val) //0 <= n <N</pre>
```

•reading into result variable:

reg.read(result,n)





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#### **Example: Inter packet gap detection**

```
register<bit<32>>(8192) flowlet lasttimeseen;
                                                              Caveat: concurrent
action flowlet gap(out bit<32> delta, bit<32> flow id)
                                                             read and write needs
{
                                                             to be synchronized if
    bit<32> last pkt seen;
                                                                   required
    /* Get the time the previous packet was seen for same flow */
    flowlet lasttimeseen.read(last pkt seen, flow id);
                                                               What is this code
    /* Calculate the time interval */
                                                                    doina?
    delta = standard_metadata.ingress_global_timestamp - last_pkt seen;
    /* Update the register with the new timestamp */
    flowlet_lasttimeseen.write(flow_id,
        standard metadata.ingress global timestamp);
    . . .
```



}

## **P4 Counters**

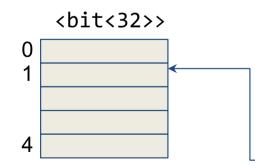
•used to count packets, bytes or both, formed in arrays•Definition:

```
counter(N, Type) my_count;
```

•Updating:

```
my_count.count(n)
```

```
enum CounterType {
    packets,
    bytes,
    packets_and_bytes
}
```



counter (5,CounterType.packets\_and\_bytes) hello;

hello.count(1); //increases counter at position 1
with current packet size information

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## Example: Count incoming packet and bytes per port

```
control MyIngress(...) {
```

```
counter(64, CounterType.packets_and_bytes) c;
```

```
apply { //ingress port number as index
    c.count((bit<32>)standard_metadata.ingress_port);
}
```

#### **Interaction from Control Plane**

```
RuntimeCmd: counter_read MyIngress.c 1 //will then return
MyIngress.c[1] = BmCounterValue(packets=1, bytes=658)
```

//Note: cannot access counter information from within data plane



}

#### **Example: counter use**

```
control MyIngress(...) {
                                            Question: What this P4 code does?
    counter(64, CounterType.packets) c;
 action tally() {
   c.count((bit<32>) standard_metadata.ingress_port); }
 table monitor {
   key = {
     hdr.ipv4.srcAddr: lpm;
                             }
                                                    "table": "MyIngress.monitor",
   actions = { tally; NoAction; } }
                                                    "match": {
                                                      "hdr.ipv4.srcAddr": ["10.0.1.1", 32]
 apply {
                                                    },
    . . .
                                                    "action name": "MyIngress.tally",
   if(hdr.ipv4.isValid()) {
                                                    "action params": { }
     . . .
                                                  }
     monitor.apply();
    }
                                                                                      26
```

## Example: using direct counters

```
control MyIngress(...) {
     direct counter(CounterType.packets) c;
 action tally() {
   c.count(); }
 table monitor {
   key = {
     hdr.ipv4.srcAddr: lpm;
                              }
   actions = { tally; NoAction; }
   counters = c;
   size =1024}
 apply {
    . . .
   if(hdr.ipv4.isValid()) {
      . .
     monitor.apply();
```

#### Direct counters are attached to tables

• Each table entry has a counter that counts upon match

## Question: What this P4 code does?

```
{
  "table": "MyIngress.monitor",
  "match": {
    "hdr.ipv4.srcAddr": ["10.0.1.1", 32]
  },
  "action_name": "MyIngress.tally",
  "action_params": { }
}
```

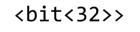


## **P4 Meters**

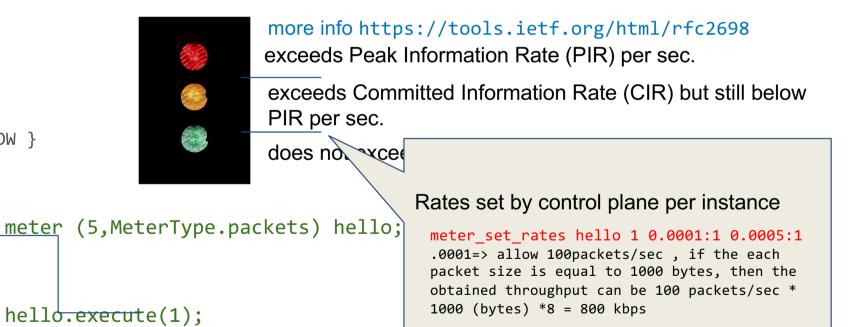
#### used to measure packet rates, can be formed in arrays

•Definition: meter(N, Type) my\_meter;

```
•Applying: my_me<sup>-</sup>
enum MeterType {
    packets,
    bytes
}
enum MeterColor {
    RED, GREEN, YELLOW }
```





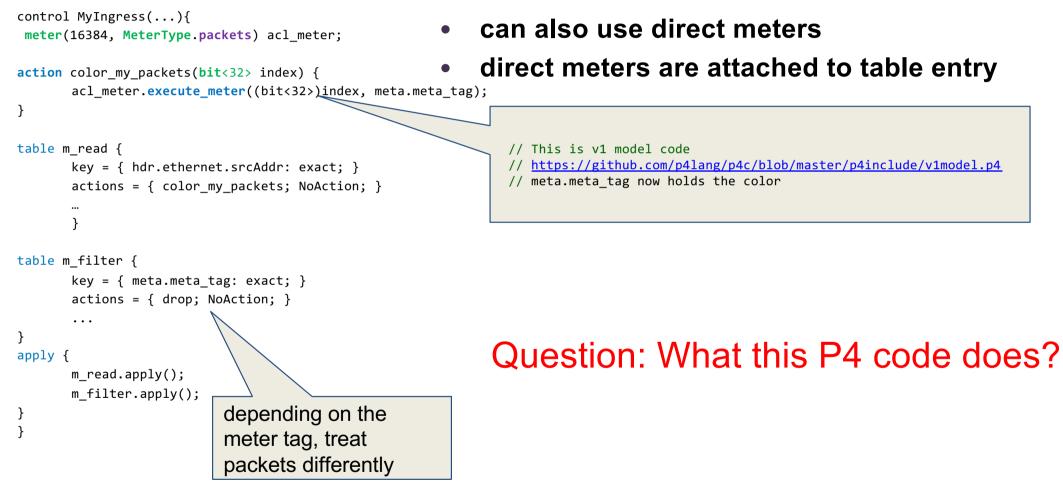




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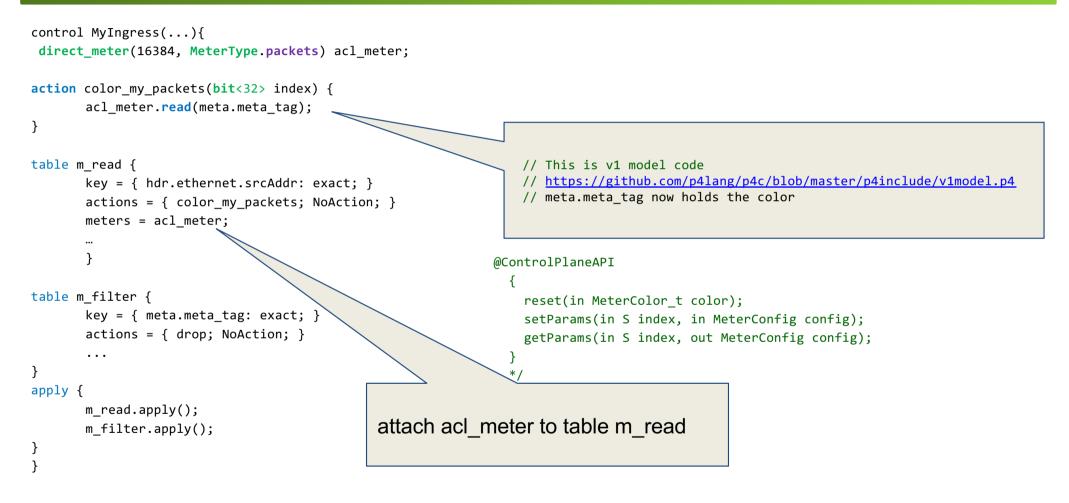
## **Example: Rate-limitig with meters**





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## **Example: Rate-limitig with direct meters**





#### Summary

•Several Stateful constructs to record and update per flow/packet state

•Useful for many things,

oe.g. congestion tracking, stateful forwarding,....

•Will see some of these concepts applied in next module

oe.g. congestion aware load-balancing



## **Module Summary - P4**

#### • Clearly defined semantics

• You can describe what your data plane program is doing

#### • Expressive

• Supports a wide range of architectures through standard methodology

#### High-level, Target-independent

- Uses conventional constructs
- Compiler manages the resources and deals with the hardware
- Type-safe
  - Enforces good software design practices and eliminates "stupid" bugs
- Agility
  - High-speed networking devices become as flexible as any software
- Insight
  - Freely mixing packet headers and intermediate results



## Module Summary - P4 things we covered

#### • The P4 world

- Protocol Independent Packet Processing
- Language/Architecture separation
- If you can interface with it, you can use it
- Key data types

#### • Constructs for packet parsing

• state-machine type

#### • Constructs for packet processing

- Actions, tables, controls
- Packet Deparsing
- Architectures and Programs



## Module Summary - P4 things we did not cover

#### • Enforcing Modularity

• Instantiating and invoking parsers or controls

#### • Variable Length field processing

parsing and deparsing of TLVs

#### • Architecture definition constructs

• How to create such template definitions

#### • Advanced features

- learning, multicast, cloning, resubmitting
- header unions
- Control Plane Interface



#### **Next Module**

#### Datacenter Load-balancing

- What is a datacenter
- What is data center networking
- Traffic characteristics in a data center
- Load-balancing techniques for data center networking
- P4 based Load-balancing
- When?
  - March 15th, 17:00 CET

